

EAST Search History

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|--------|--|---|------------------|---------|------------------|
| L8 | 388038 | (TFT near lcd) or (liquid adj crystal adj display) or lcd or TFT-LCD | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2006/03/20 08:04 |
| L9 | 331174 | gate with (TFT or switch\$5 or transistor or logic) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2006/03/20 08:04 |
| L10 | 29223 | 9 with resistor | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2006/03/20 08:05 |
| L11 | 120 | 8 same 10 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2006/03/20 08:17 |
| L12 | 6502 | TFT-LCD or (tft near lcd) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2006/03/20 08:17 |
| L13 | 0 | 12 same (resistor near2 gate) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2006/03/20 08:17 |
| L14 | 10840 | (resistor near2 gate) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2006/03/20 08:18 |
| L15 | 115 | 14 same display | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2006/03/20 08:18 |
| L16 | 77 | 15 same (transistor or TFT or switch or logic) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2006/03/20 08:19 |
| S1 | 1310 | 345/92,98,99,100.ccls. | USPAT | OR | OFF | 2005/09/29 09:45 |
| S2 | 647 | TFT and 345/92,98,99,100.ccls. | USPAT | OR | OFF | 2002/12/11 13:11 |

EAST Search History

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| S3 | 117 | (TFT and 345/92,98,99,100.ccls.) and (resistor\$1) and capacitor\$1 | USPAT | OR | OFF | 2002/12/11 14:18 |
| S4 | 187 | 345/90.ccls. | USPAT | OR | OFF | 2002/12/11 14:18 |
| S5 | 98 | 345/90.ccls. and TFT | USPAT | OR | OFF | 2002/12/11 14:18 |
| S6 | 21 | (345/90.ccls. and TFT) and capacitor\$1 and resistor\$1 | USPAT | OR | OFF | 2002/12/11 14:23 |
| S7 | 132 | TFT same capacitor\$1 same resistor\$1 | USPAT | OR | OFF | 2002/12/11 14:45 |
| S8 | 338 | TFT and capacitors and resistors | USPAT | OR | OFF | 2002/12/11 14:45 |
| S9 | 110 | "345"/\$.ccls. and (TFT and capacitors and resistors) | USPAT | OR | OFF | 2002/12/11 14:45 |
| S10 | 0 | scan same (flicker or cross adj talk)same resistor same capacitor and TFT | USPAT | OR | OFF | 2002/12/11 15:06 |
| S11 | 1 | (flicker or cross adj talk)same resistor same capacitor and TFT | USPAT | OR | OFF | 2002/12/11 15:06 |
| S12 | 366 | TFT and (flicker or cross adh talk) and capacitor and resistor | USPAT | OR | OFF | 2002/12/11 15:07 |
| S13 | 79 | TFT and (flicker or cross adj talk) and capacitor and resistor | USPAT | OR | OFF | 2002/12/11 15:35 |
| S14 | 30 | resistor with scan adj line | USPAT | OR | OFF | 2002/12/11 15:37 |
| S15 | 8 | resistor same (scan adj line) and TFT | USPAT | OR | OFF | 2005/09/29 09:34 |
| S16 | 1 | resistor with (scan adj line) and TFT | USPAT | OR | OFF | 2002/12/11 15:40 |
| S17 | 2900 | TFT adj LCD | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2003/05/28 12:38 |
| S18 | 0 | (resistor\$1 adj gate) and (TFT adj LCD) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2003/05/28 12:41 |
| S19 | 46 | (resistor\$1 with gate) and (TFT adj LCD) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2003/05/28 12:56 |
| S20 | 2 | (resistor\$1 with (gate with scan\$4)) and (TFT adj LCD) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2003/05/28 12:56 |

EAST Search History

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| S21 | 2 | "6421038".pn. | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2003/05/28 14:14 |
| S22 | 2 | "5534860".pn. | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2003/05/28 14:16 |
| S23 | 2 | resistor with gate with (TFT) same scan\$5 adj line | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2003/05/28 14:18 |
| S24 | 7 | resistor same gate same (TFT) same (scan\$5 adj line) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2003/05/28 14:18 |
| S25 | 2 | gate adj voltage adj deformation | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/09/28 17:14 |
| S26 | 3486 | TFT same capacitor same storage adj capacitor | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/09/28 17:14 |
| S27 | 42 | S26 same register | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/09/28 17:19 |
| S28 | 43 | tft same capacitor same register same storage | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/09/28 17:19 |
| S29 | 4 | reisistor with transistor | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/09/28 17:20 |
| S30 | 549 | tft with resistor | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/09/28 17:20 |

EAST Search History

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|-----|--------|---|---|----|-----|------------------|
| S31 | 47 | S30 same lcd | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/09/28 17:25 |
| S32 | 3543 | scan\$6 same (tft or transistor or switch\$6) same resistor | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/09/28 17:26 |
| S33 | 446 | S32 same display | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/09/28 17:26 |
| S34 | 336 | S33 and @ad<"20000406" | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/09/28 17:35 |
| S35 | 123133 | resistor near5 capacitor | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/09/28 17:36 |
| S36 | 262 | S35 same lcd | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/09/28 17:36 |
| S37 | 192 | S36 and @ad<"20010405" | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/09/28 17:36 |
| S38 | 32 | S37 and "345"/\$.ccis. | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/09/28 17:36 |
| S39 | 50830 | gate with resistor | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/09/29 09:35 |
| S40 | 35653 | S39 same (TFT oe transistor or switch\$5) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/09/29 09:35 |

EAST Search History

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| S41 | 35651 | S39 same (TFT or transistor or switch\$5) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/09/29 09:35 |
| S42 | 84 | S41 same lcd | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/09/29 09:35 |
| S43 | 18583 | capacitors with resistor | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/09/29 09:46 |
| S44 | 5060 | S43 with (TFT or transistor or switch\$5) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/09/29 09:46 |
| S45 | 60 | S44 same display | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/09/29 09:46 |